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Title of Invention

METHOD AND STRUCTURE FOR PROVIDING TUNED LEAKAGE
CURRENT IN CMOS INTEGRATED CIRCUIT

Submission Type : Utility Patent Filing

Application Number:

10/710006



EFS ID:

62449

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First Named Applicant: Brent Anderson

Attorney Docket Number: BUR920040019US1

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-assignment	BUR920040019US1-usassn.xml	1902	2004-06-11
us-assignment	us-assignment.xml	8983	2004-06-11
us-assignment	us-assignment.dtd	10898	2004-06-11
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us-assignment	BUR920040019Assign2.tif	7656	2004-06-11
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us-declaration	BUR920040019Dec1.tif	57748	2004-06-11

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